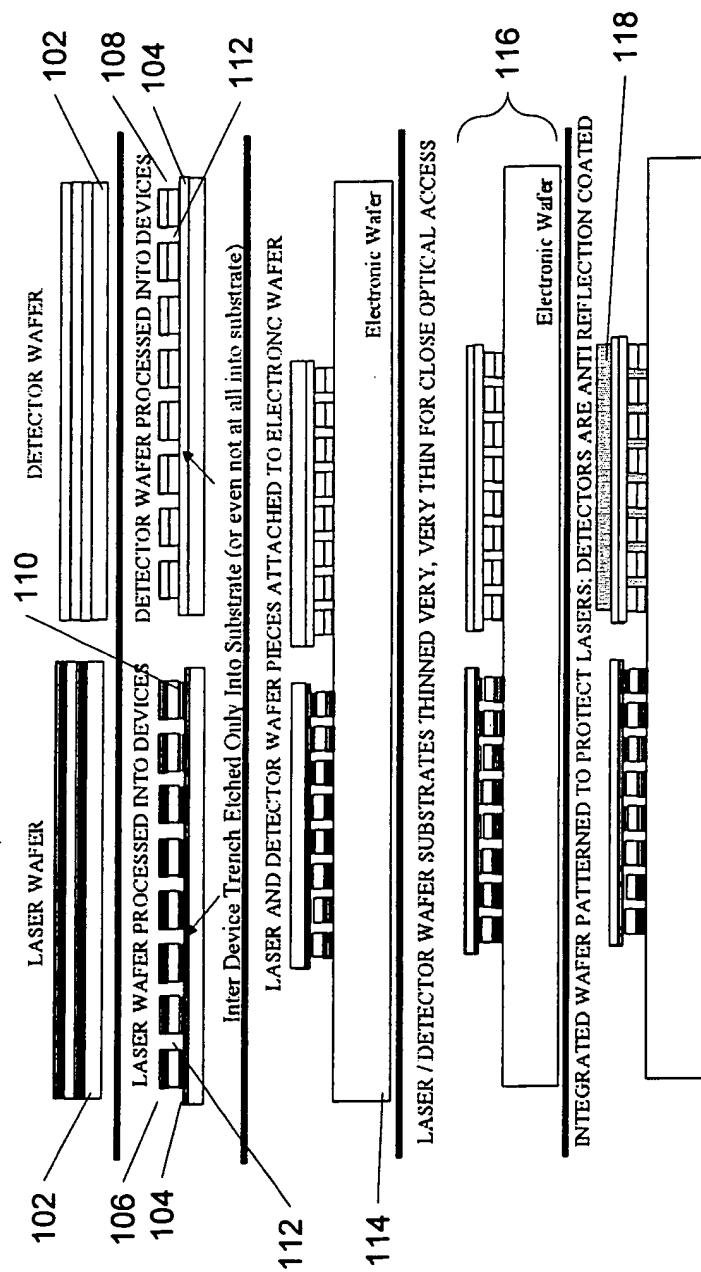


FIG. 1



113

FIG. 2

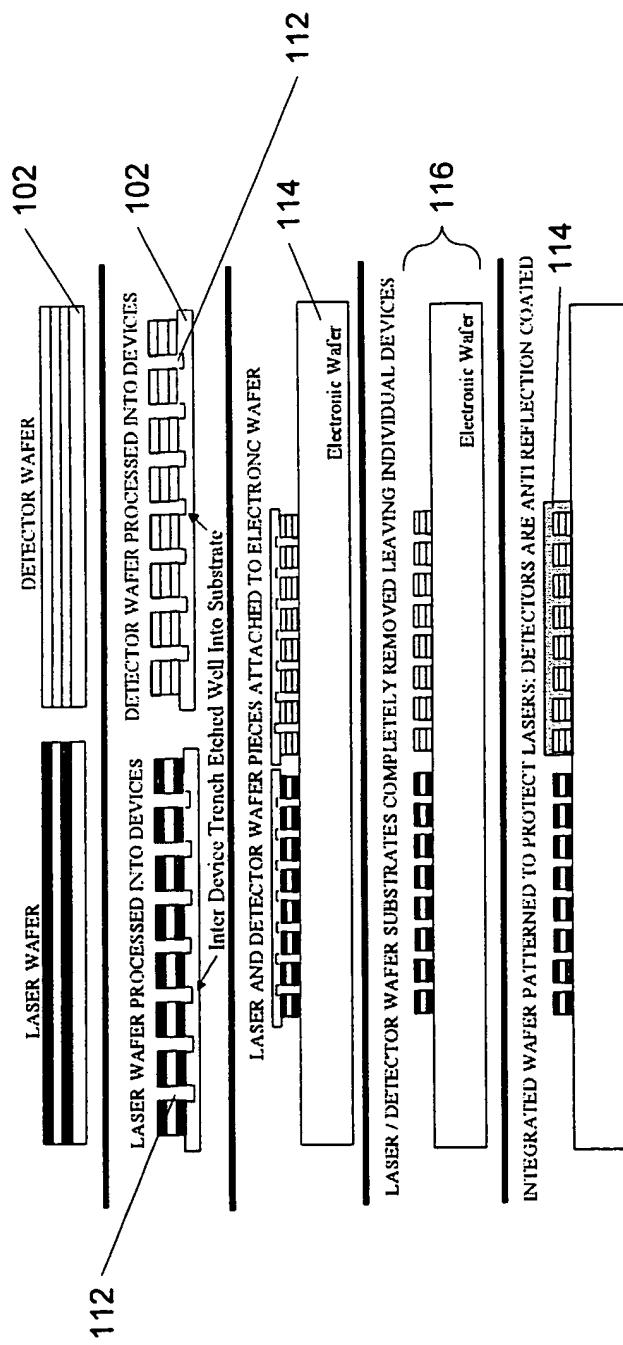


FIG. 3

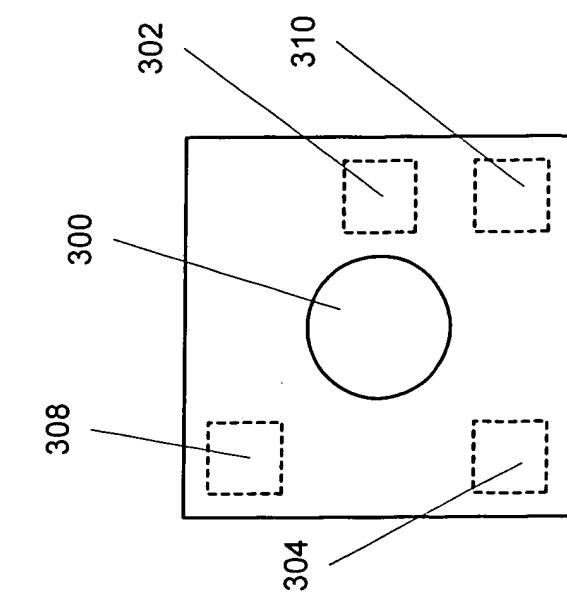
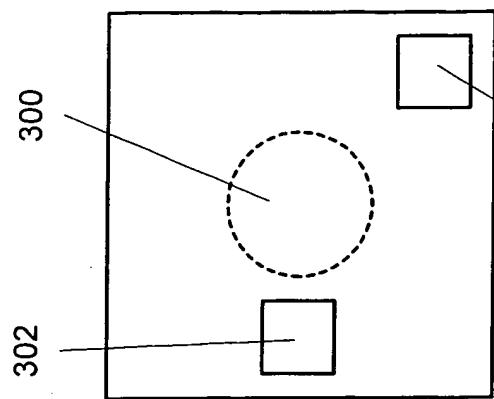
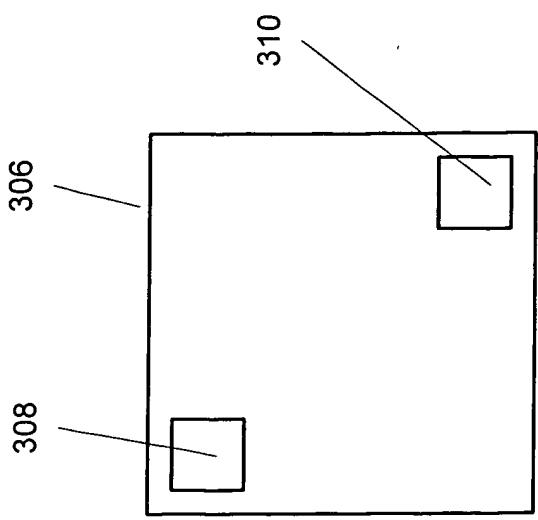


FIG. 4

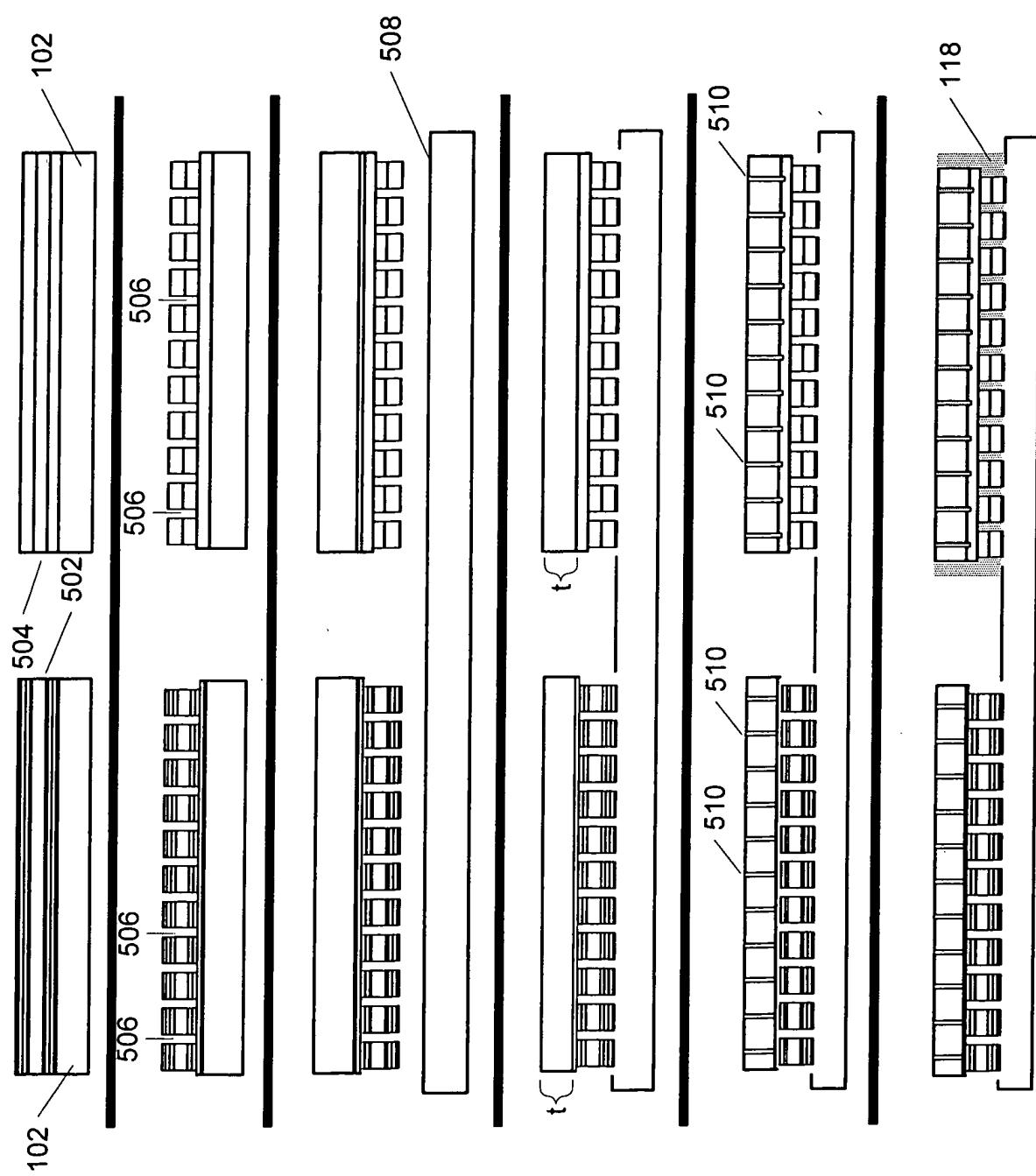


FIG. 5

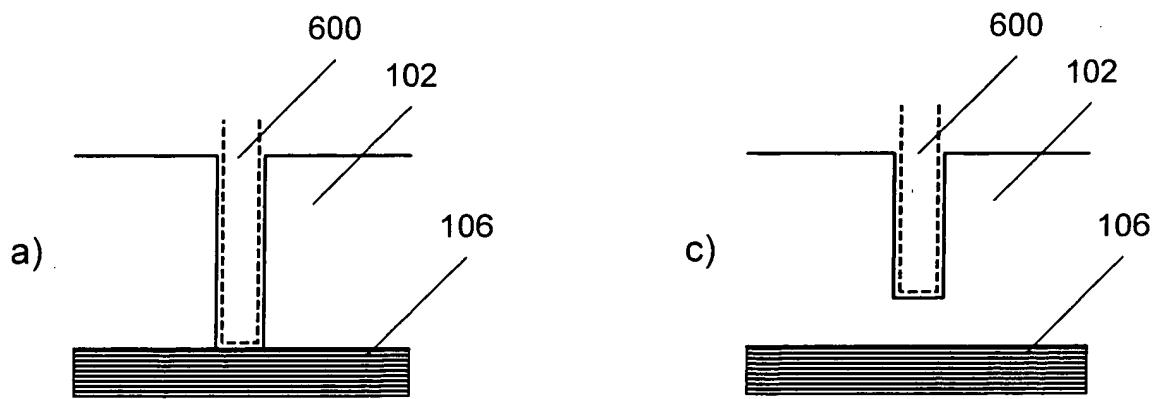
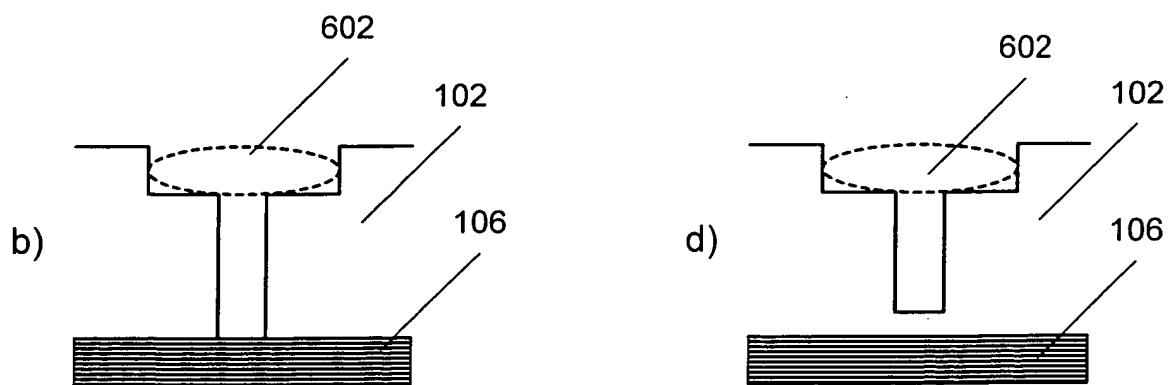


FIG. 6



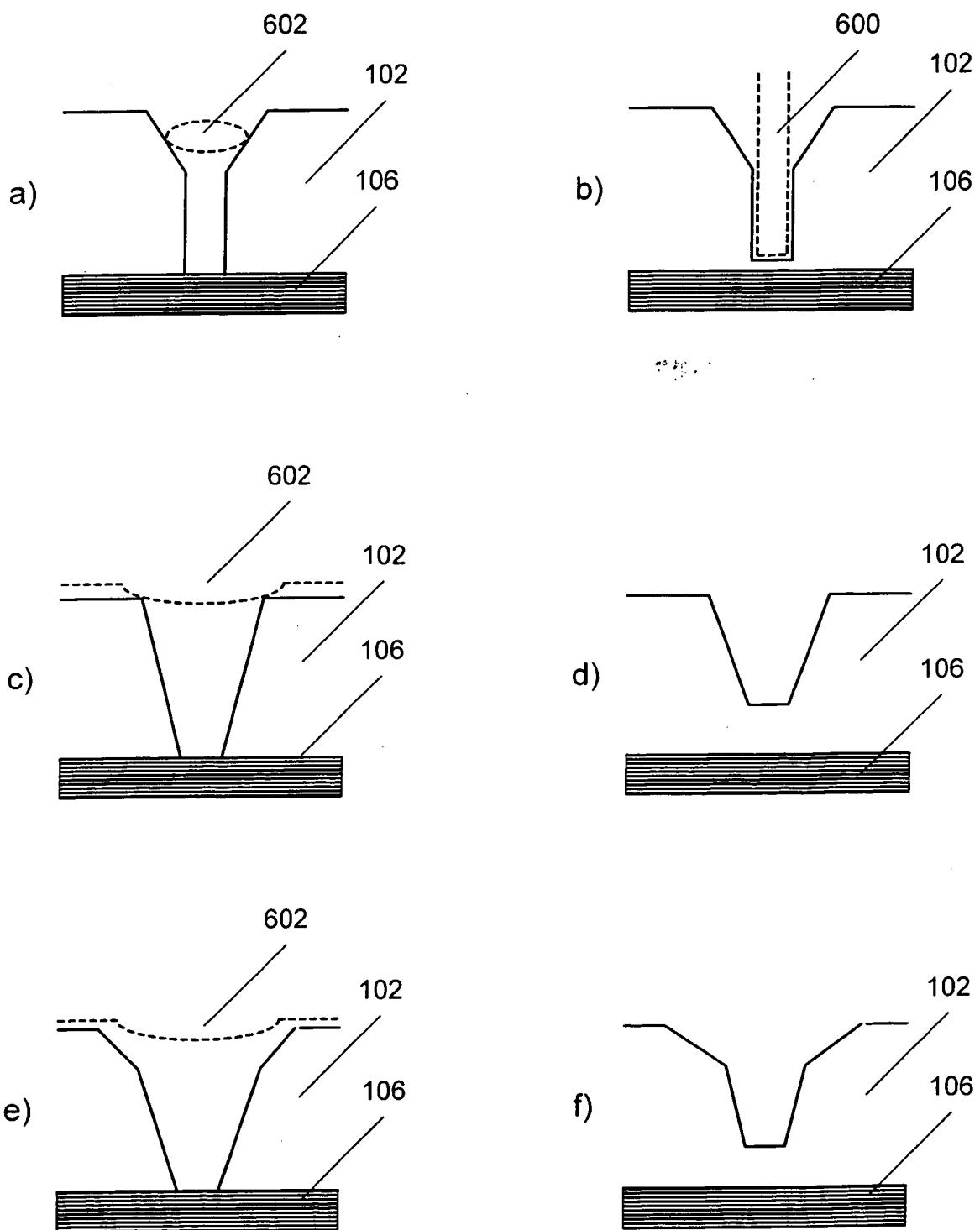


FIG. 7

600

FIG. 8

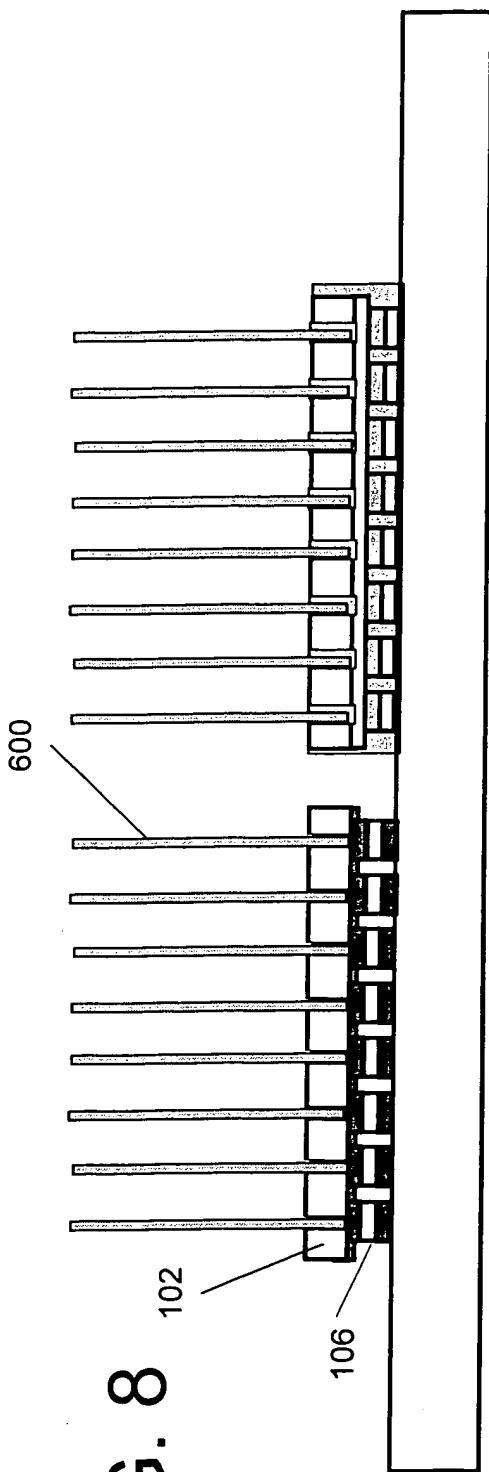
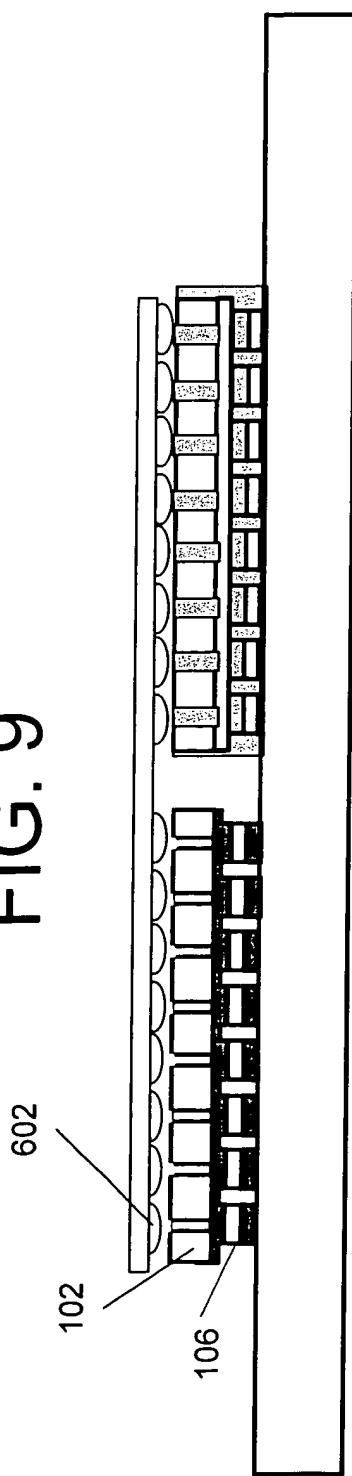
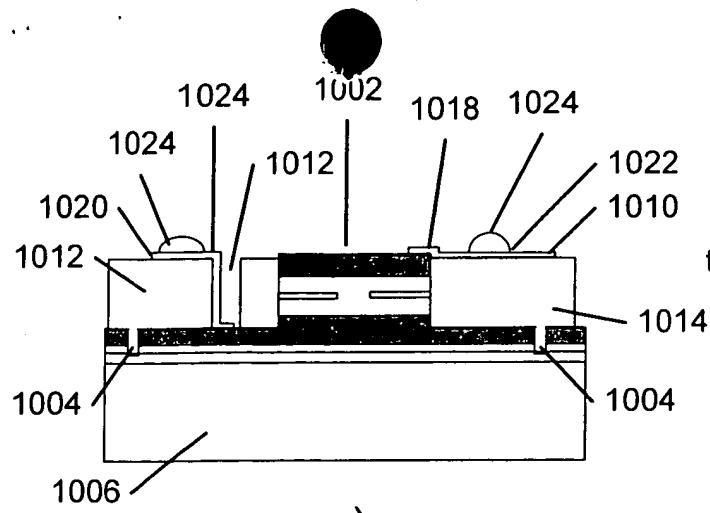
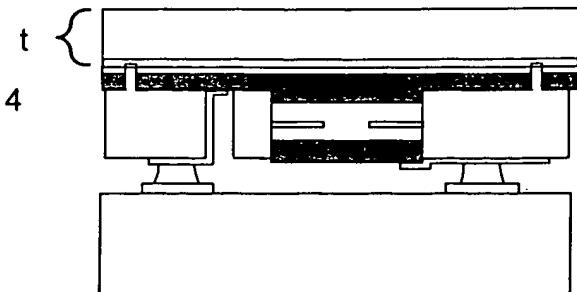


FIG. 9

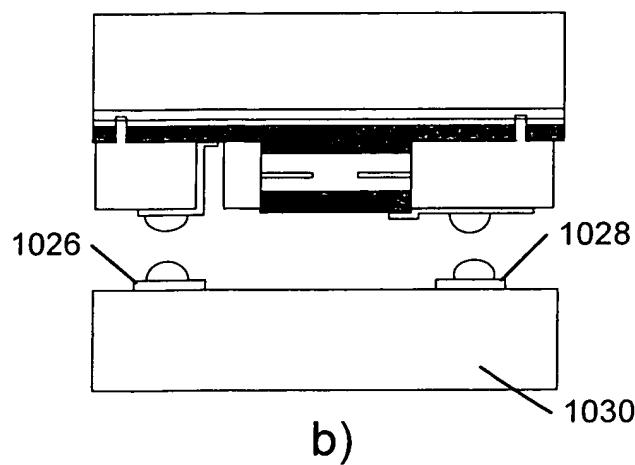




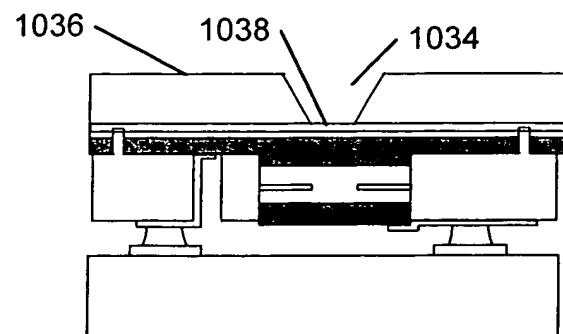
a)



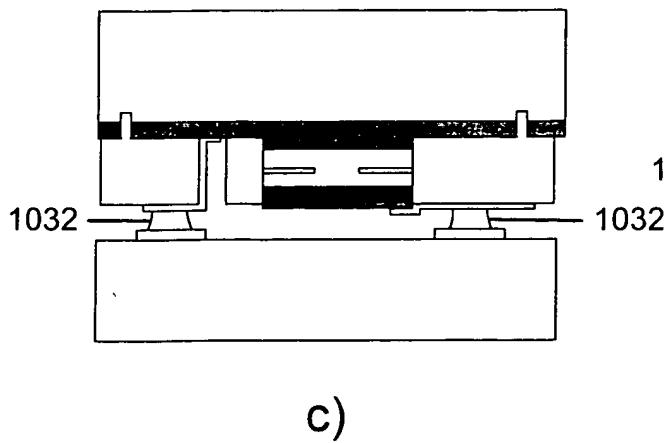
d)



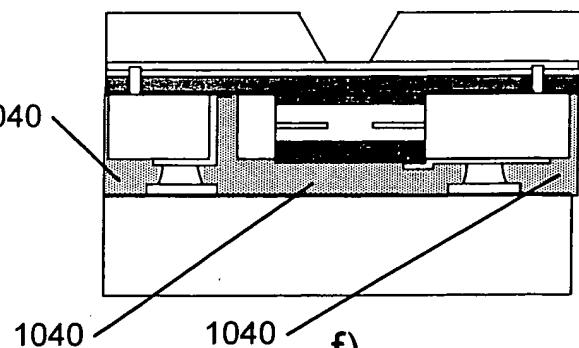
b)



e)

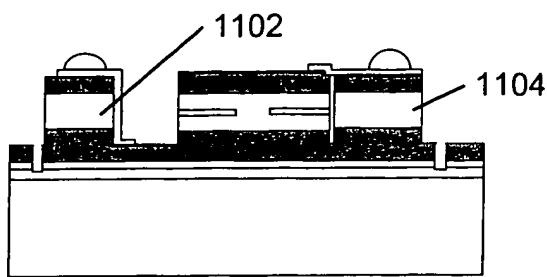


c)

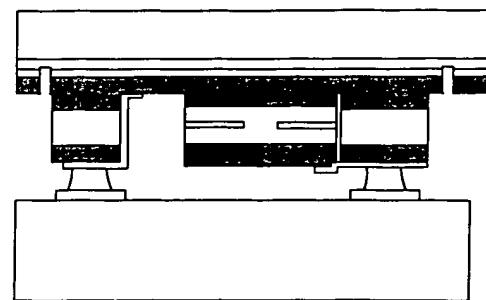


f)

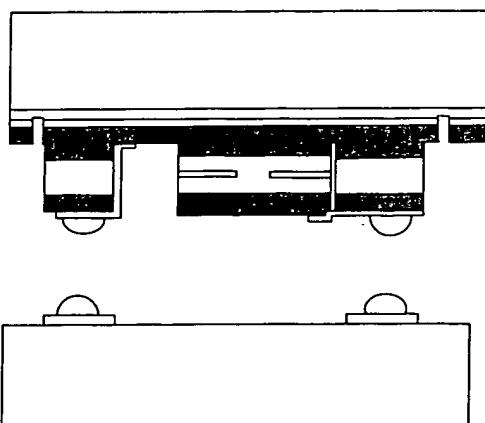
FIG. 10



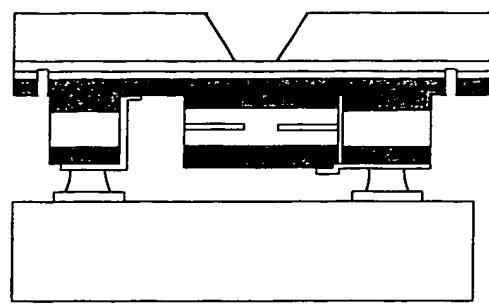
a)



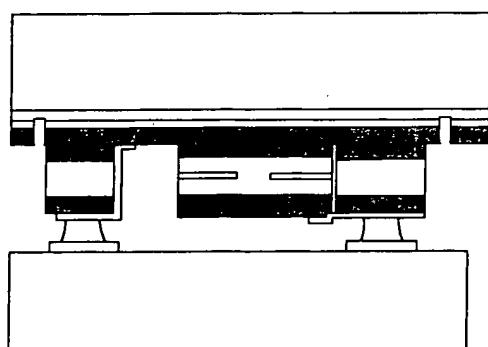
d)



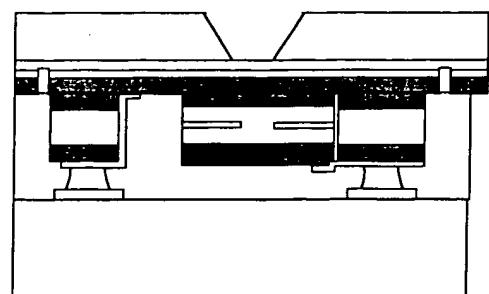
b)



e)

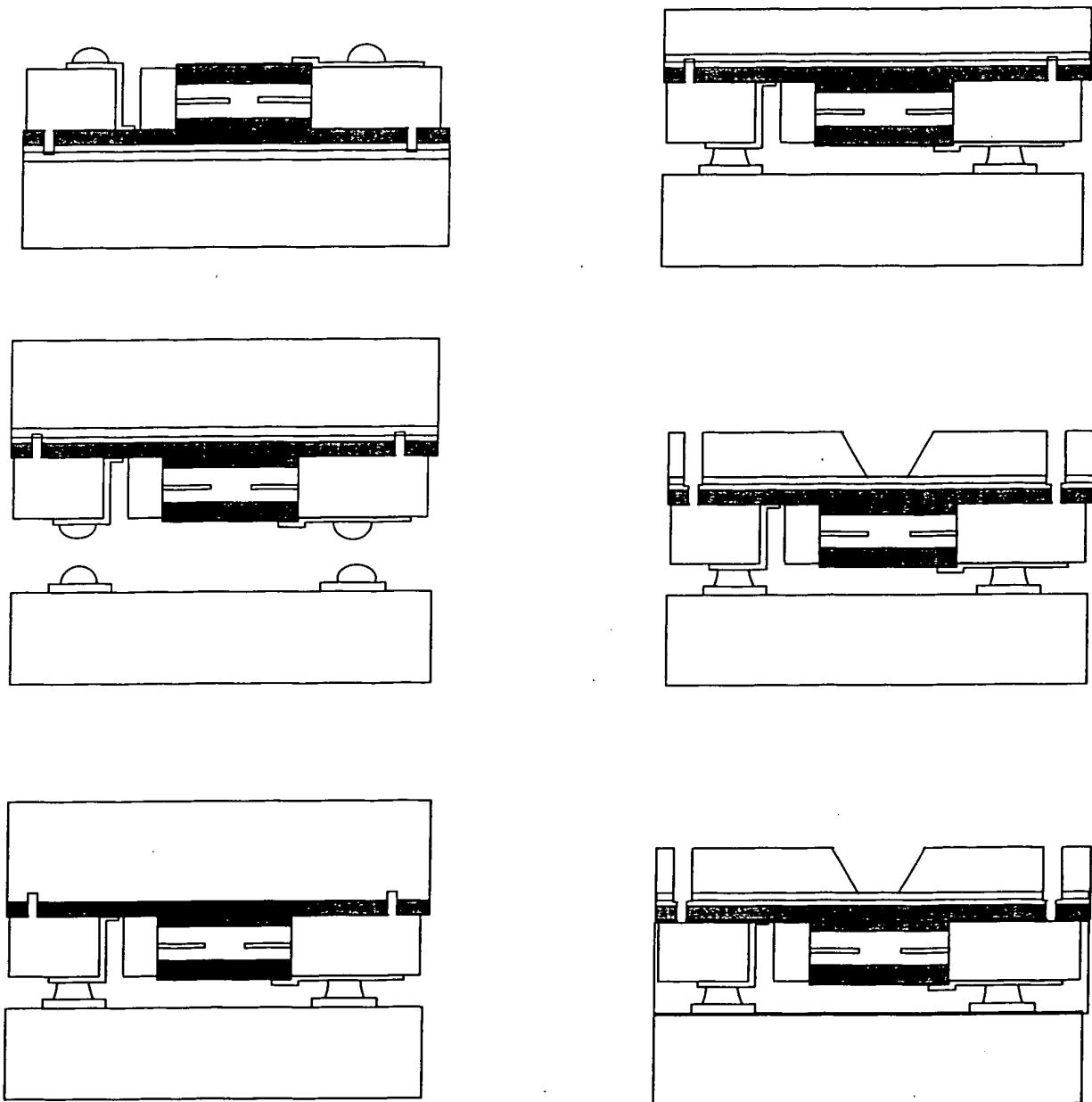


c)

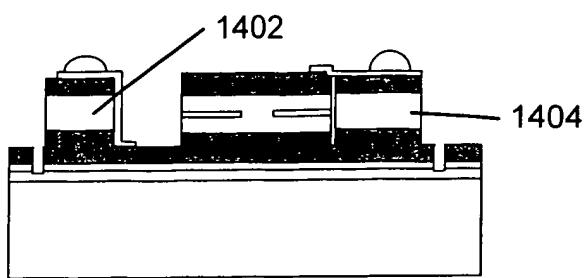


f)

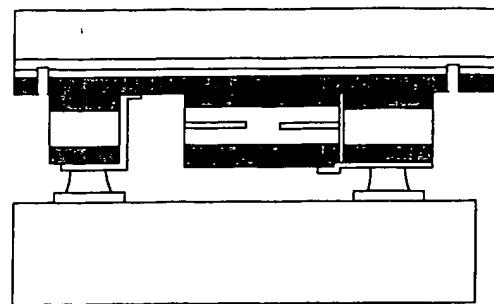
FIG. 11



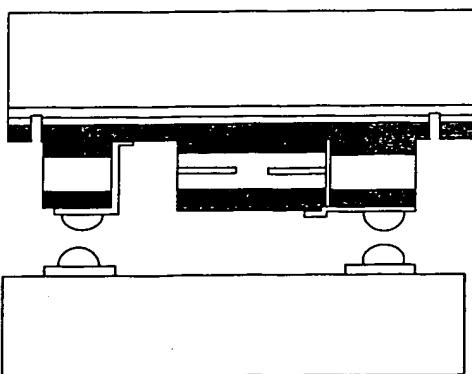
**FIG. 12**



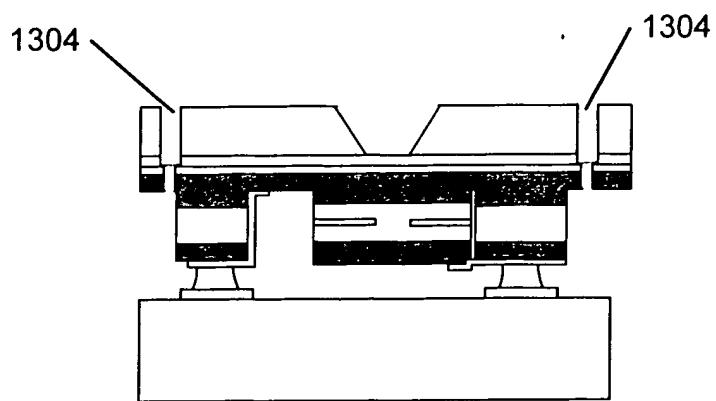
a)



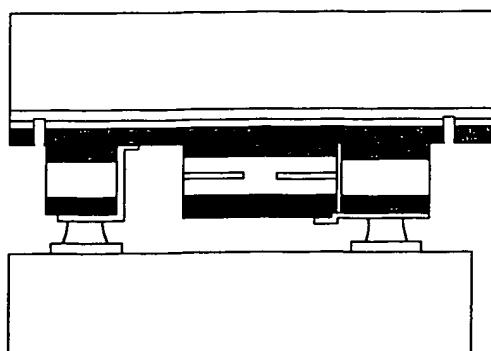
d)



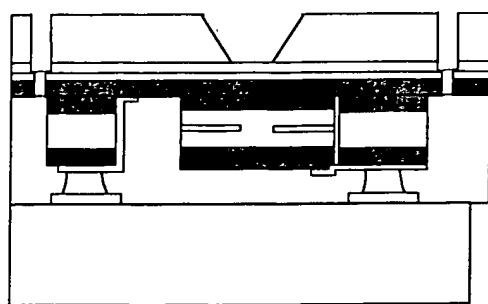
b)



e)



c)



f)

FIG. 14

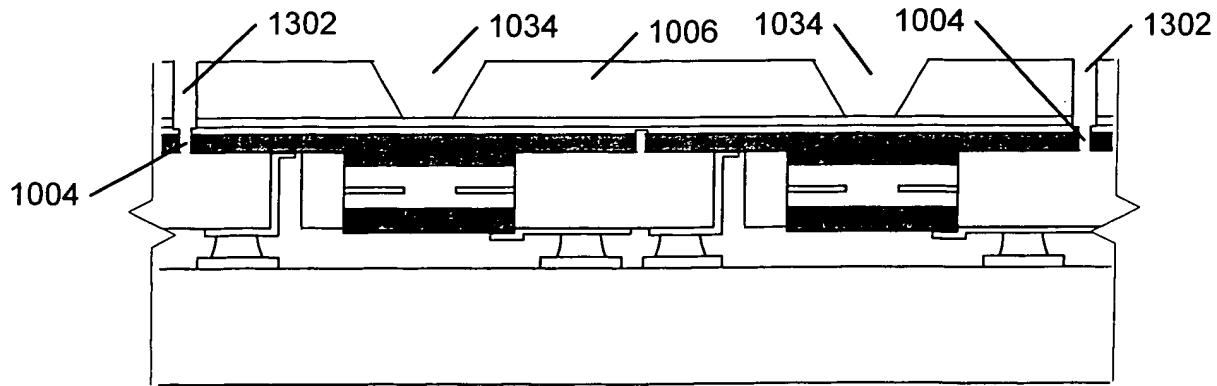
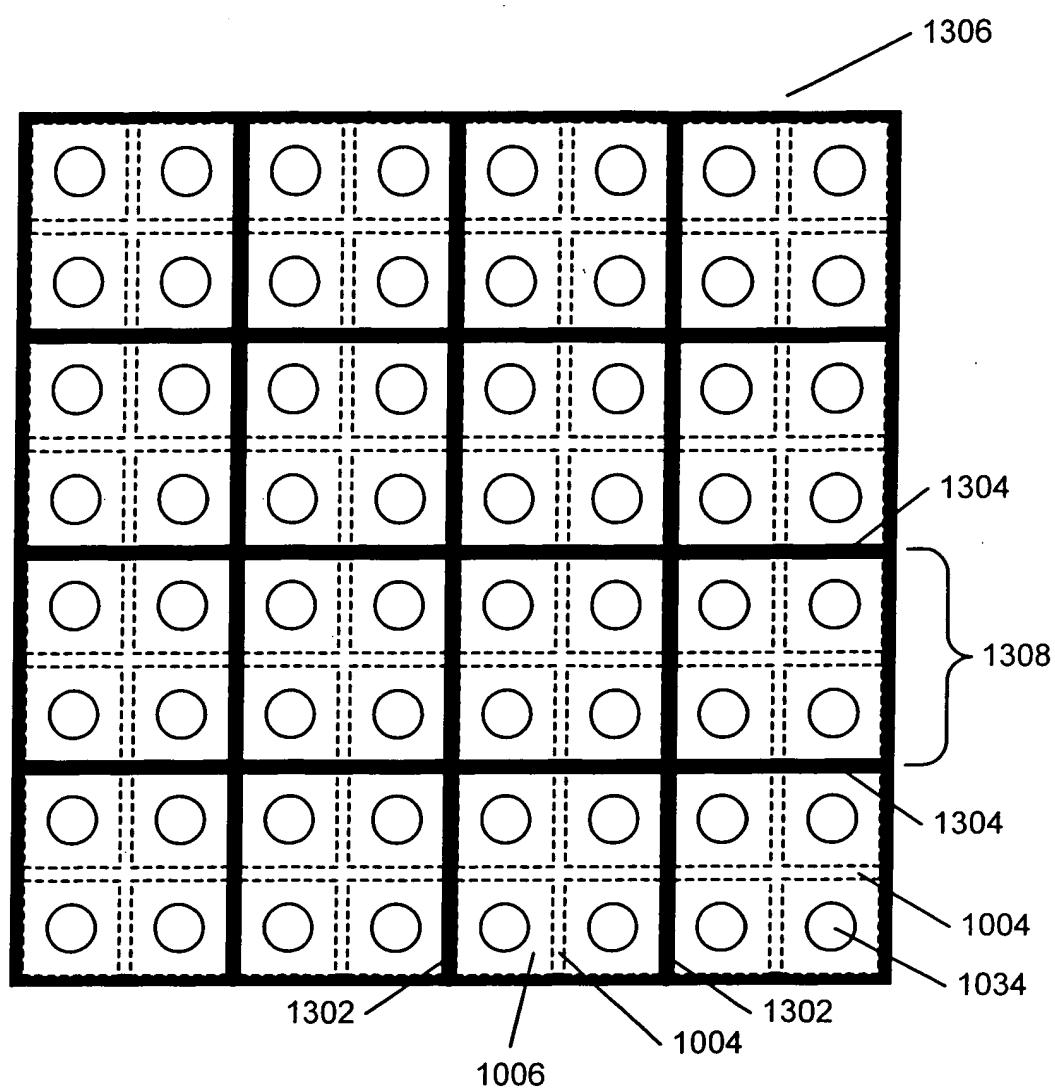
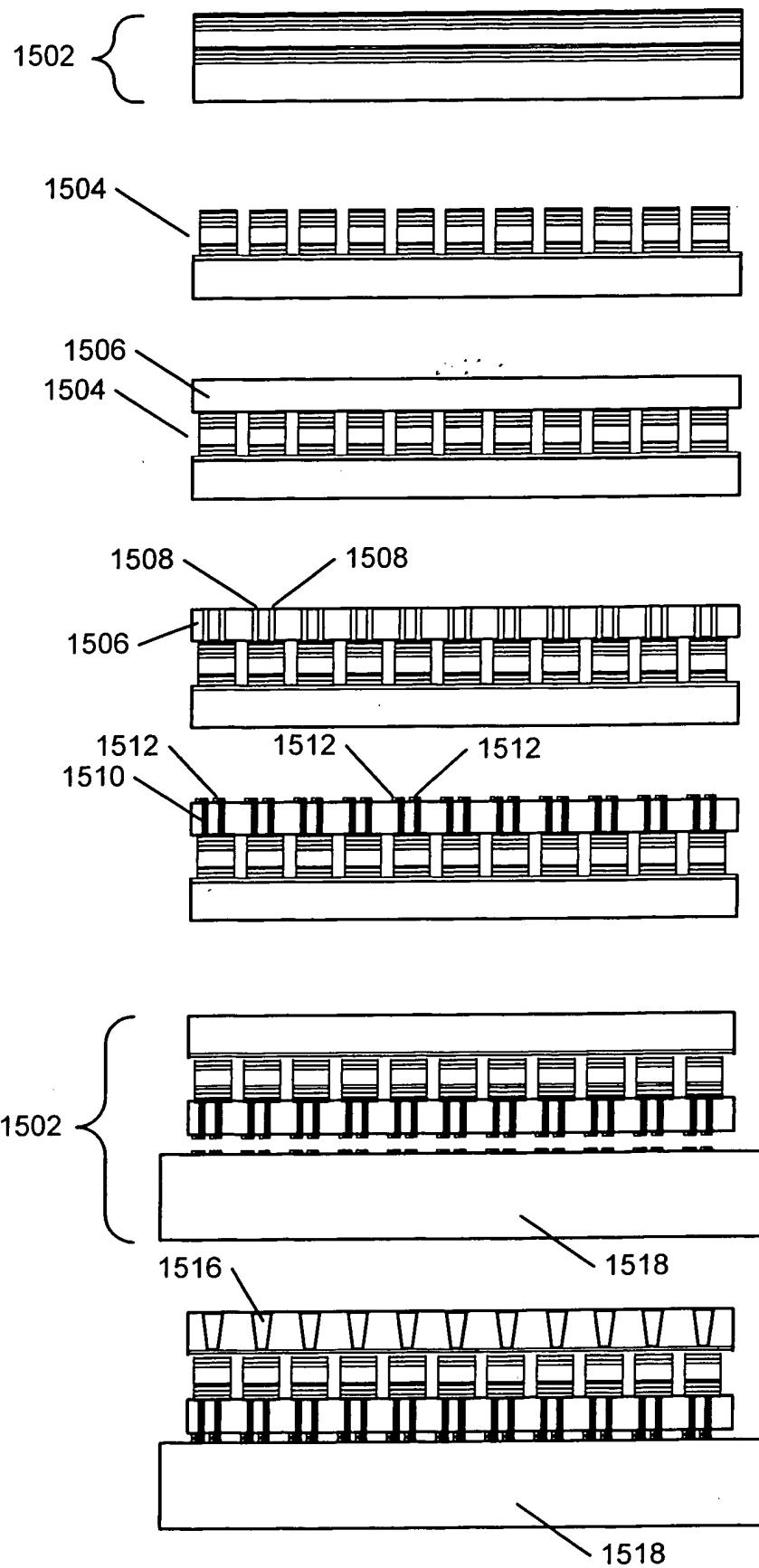


FIG. 13

**FIG. 15**



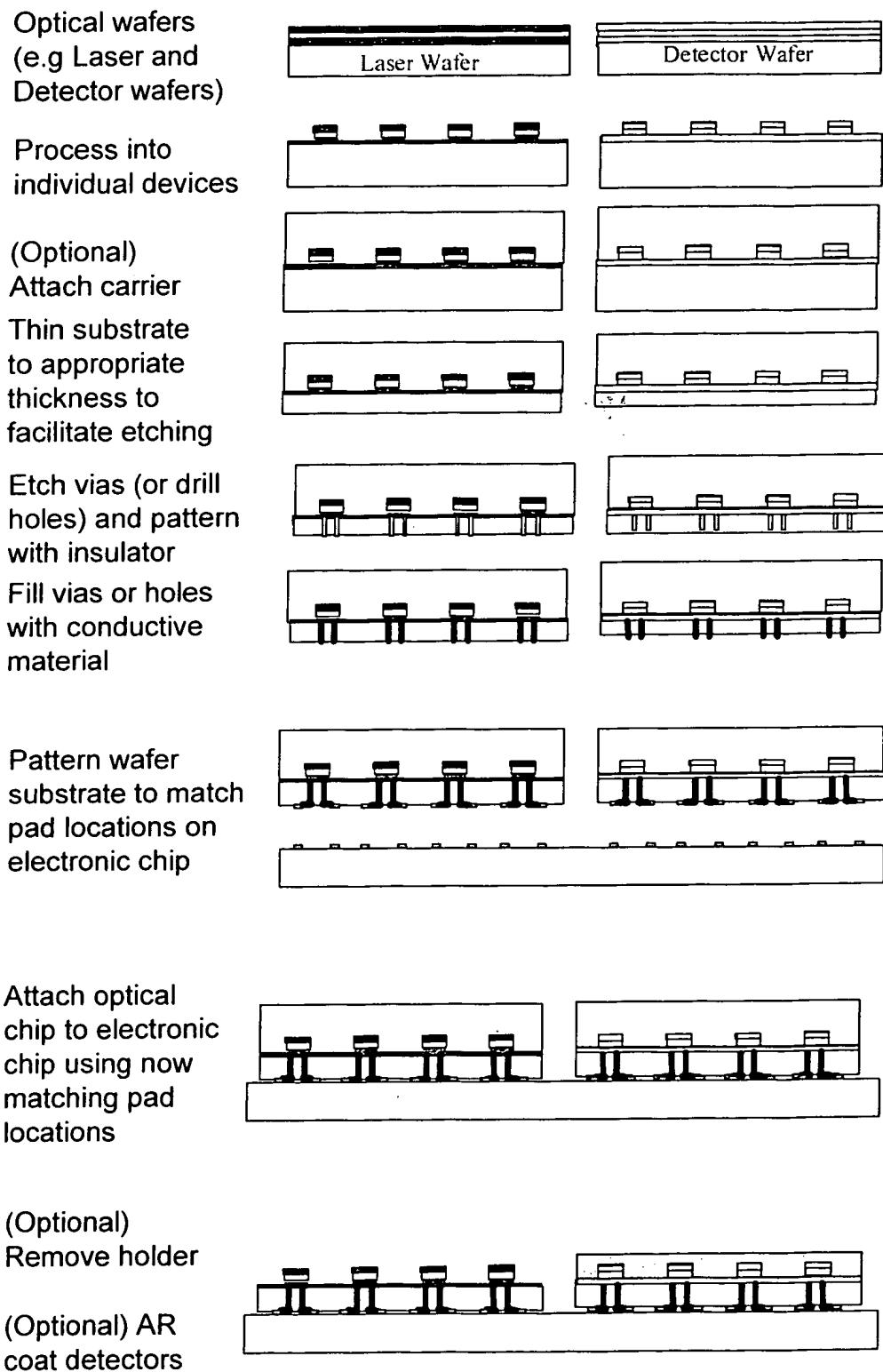


FIG. 16A

# FIG. 16B

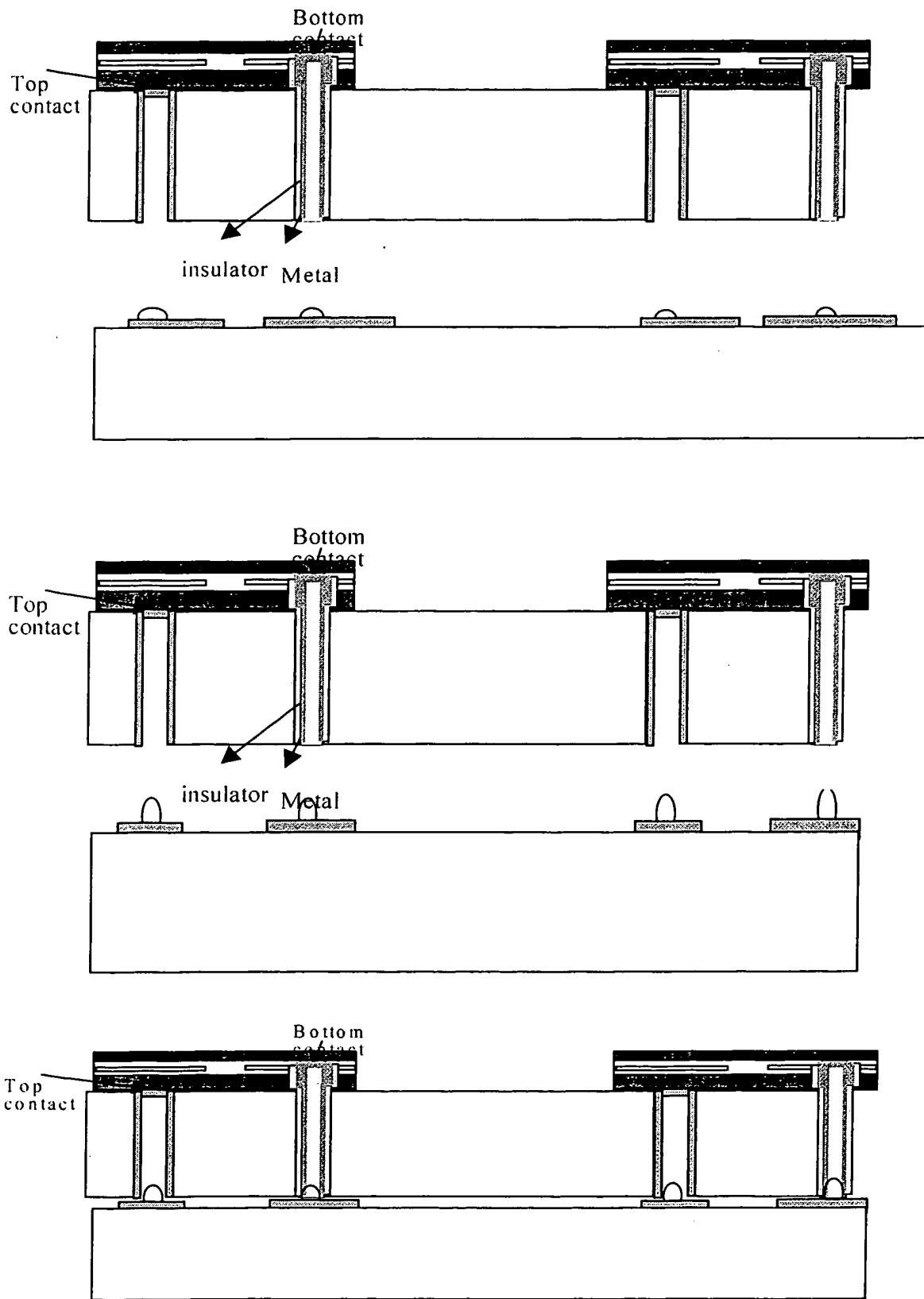
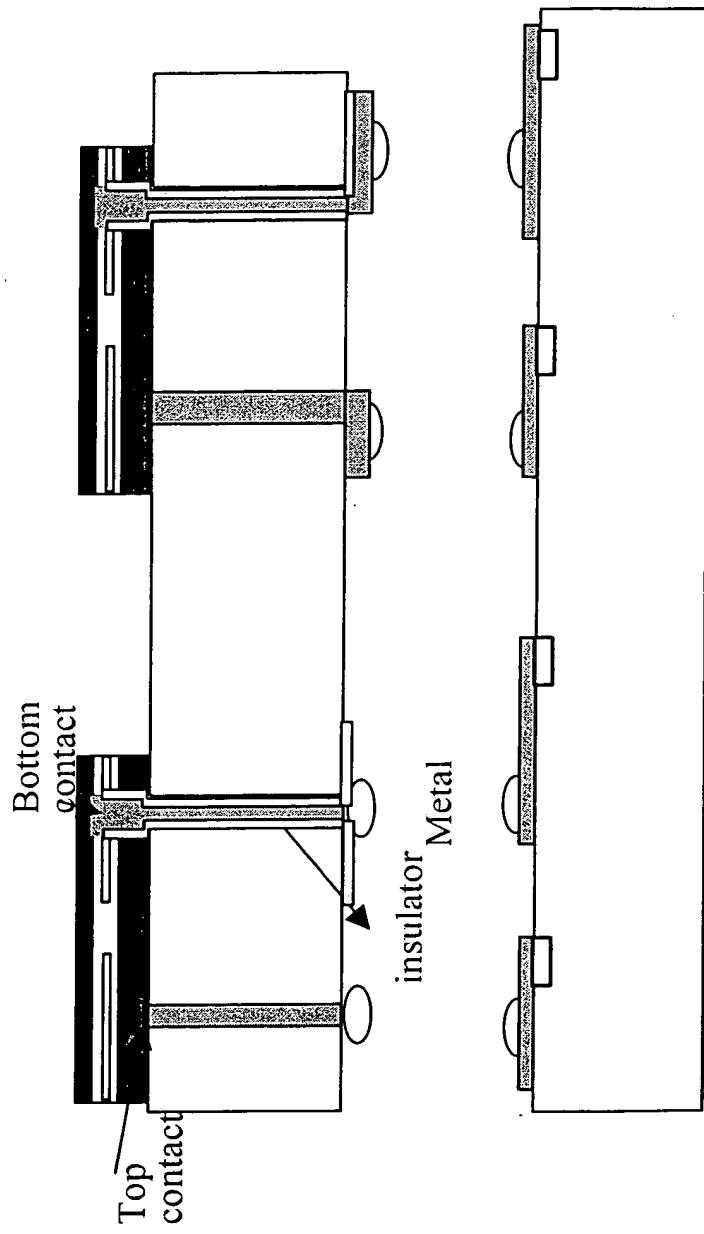


FIG. 16C



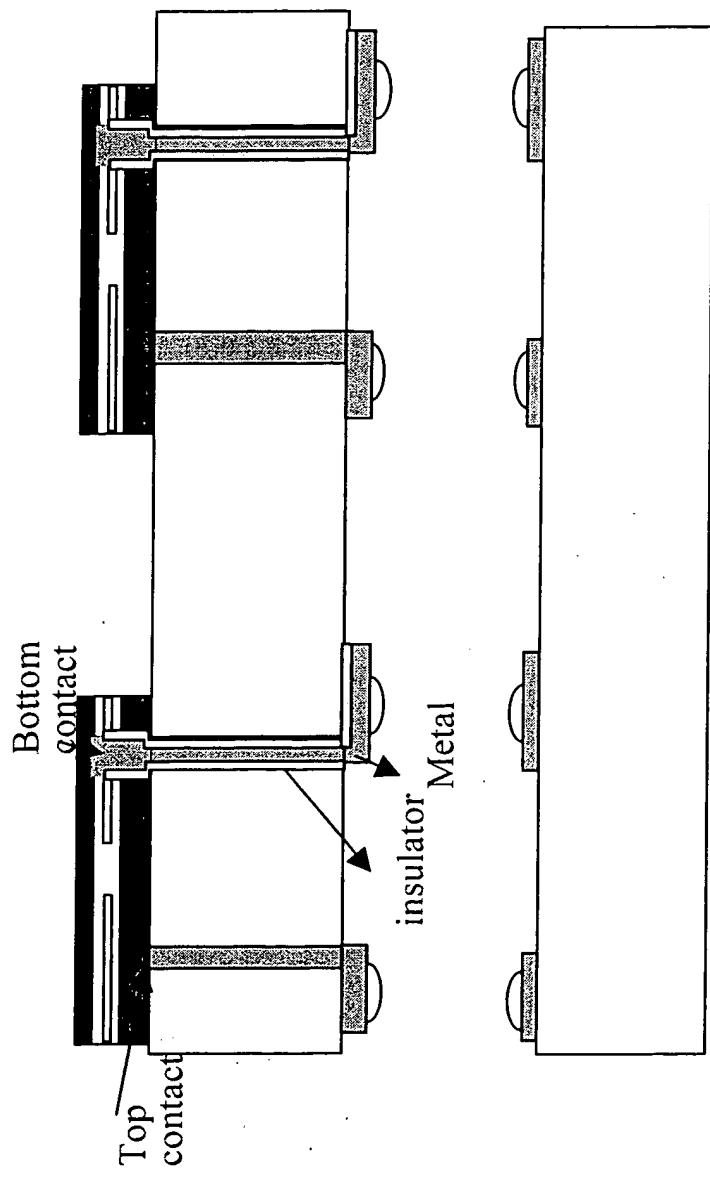
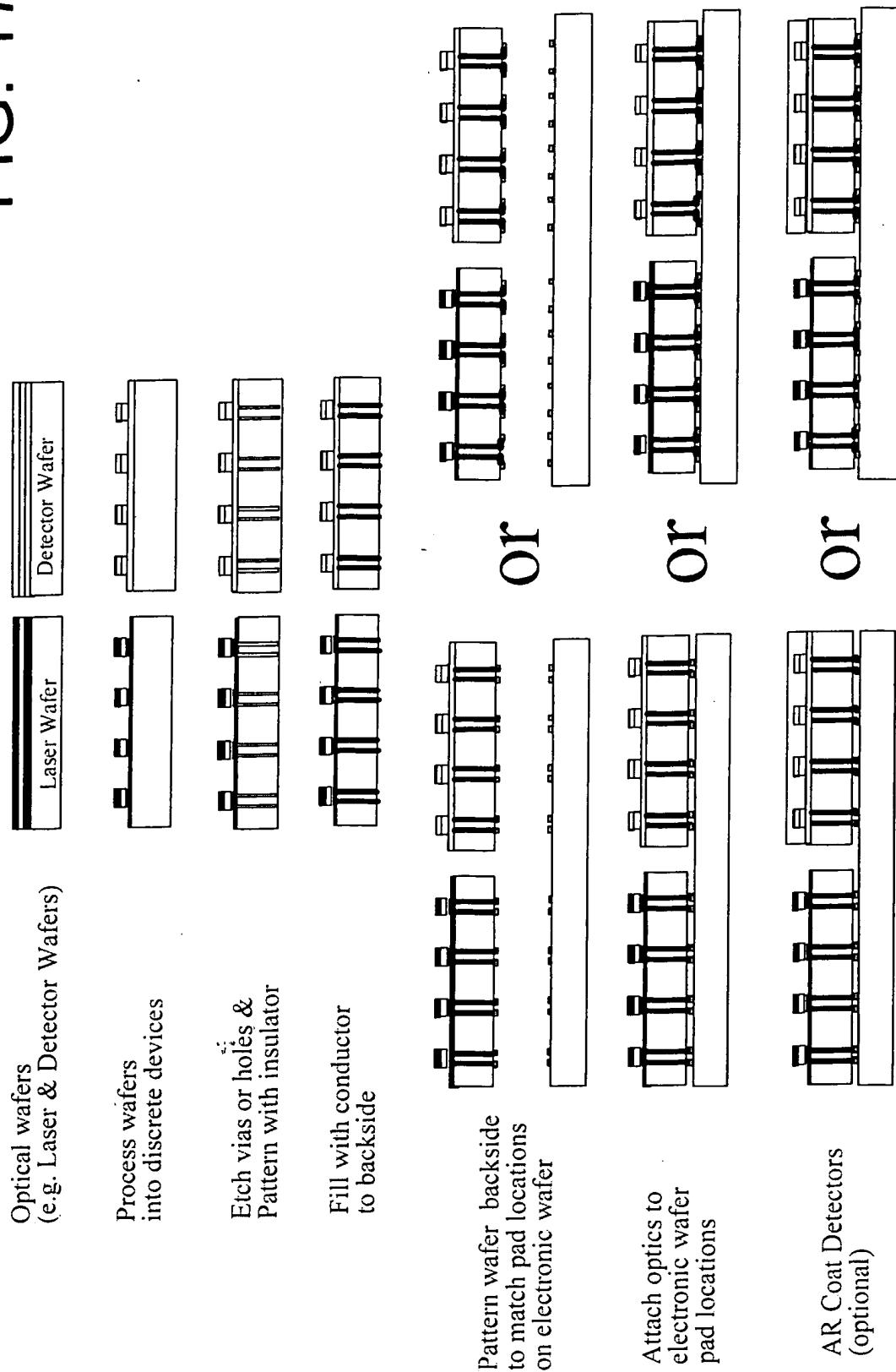


FIG. 16D

FIG. 17



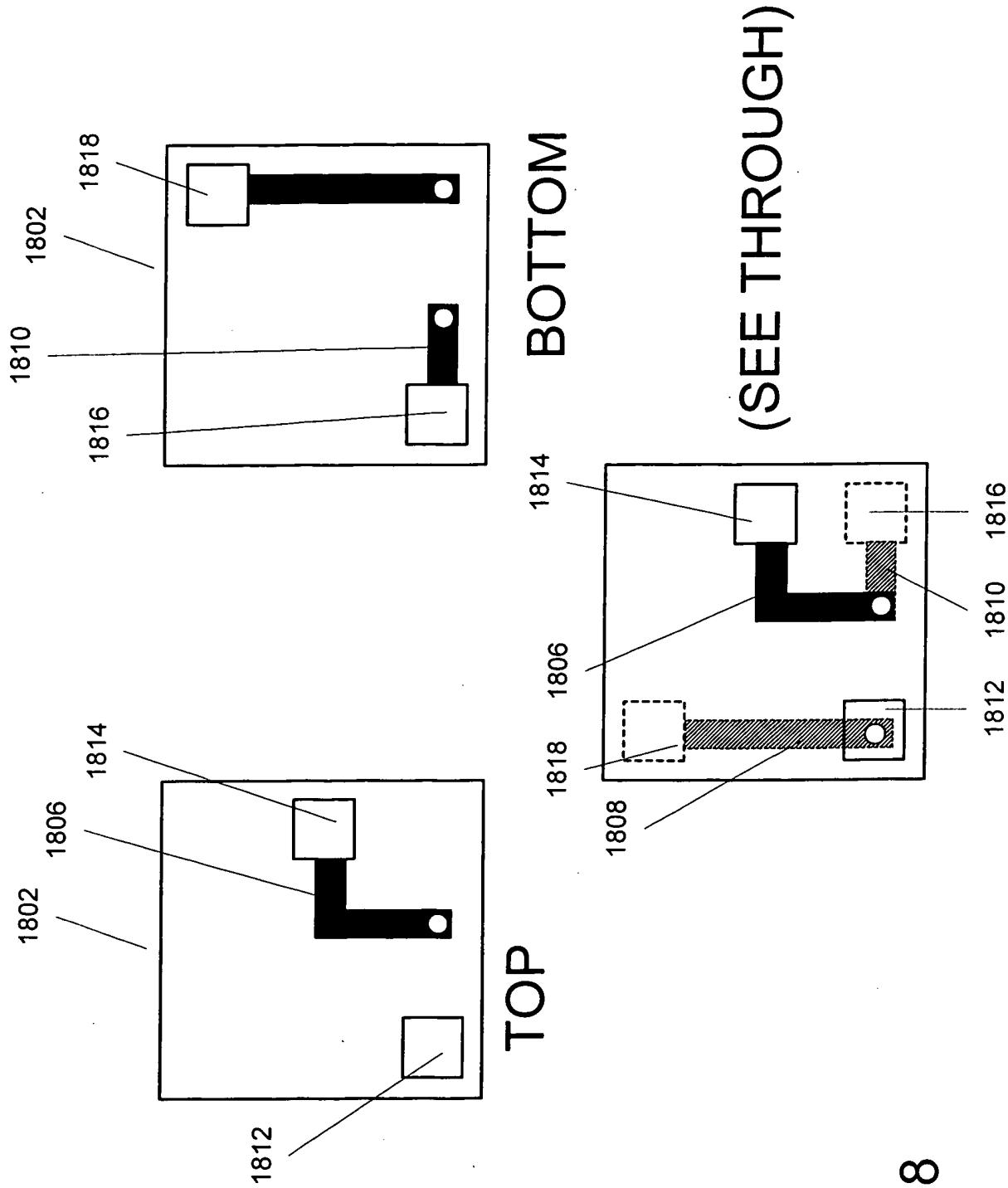
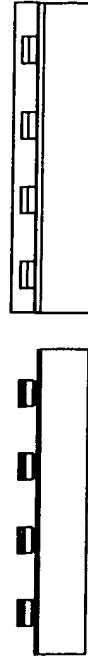


FIG. 18

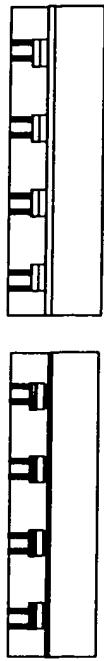
Optical wafers  
(e.g. Laser & Detector wafers)



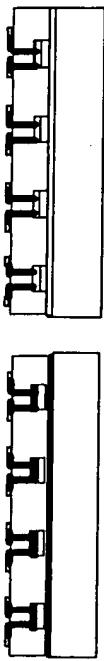
Process into individual devices  
& AR coat detectors



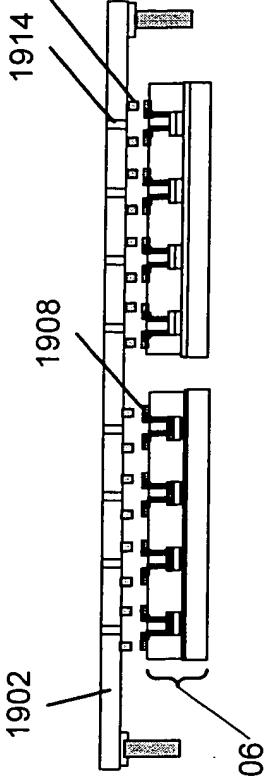
Cover devices with insulator and  
pattern vias through to device  
contact pads



Pattern optical device wafer  
to create traces to locations  
that will match mating  
contacts on adapter



Attach optical device chip  
to adapter via aligning  
pads created by patterning  
(NOTE: Holes in adapter can be  
created pre-post or concurrently  
with wafer patterning)



Integrate adapter chip with  
electronic chip via standoffs,  
wires, etc.

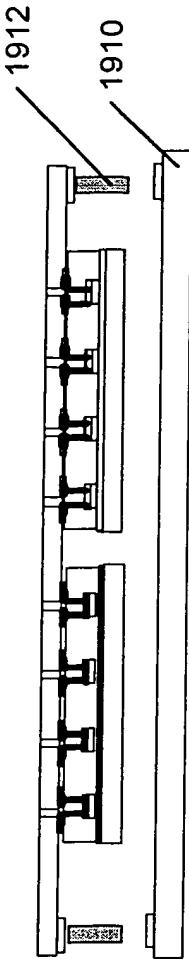


FIG. 19

FIG. 20A

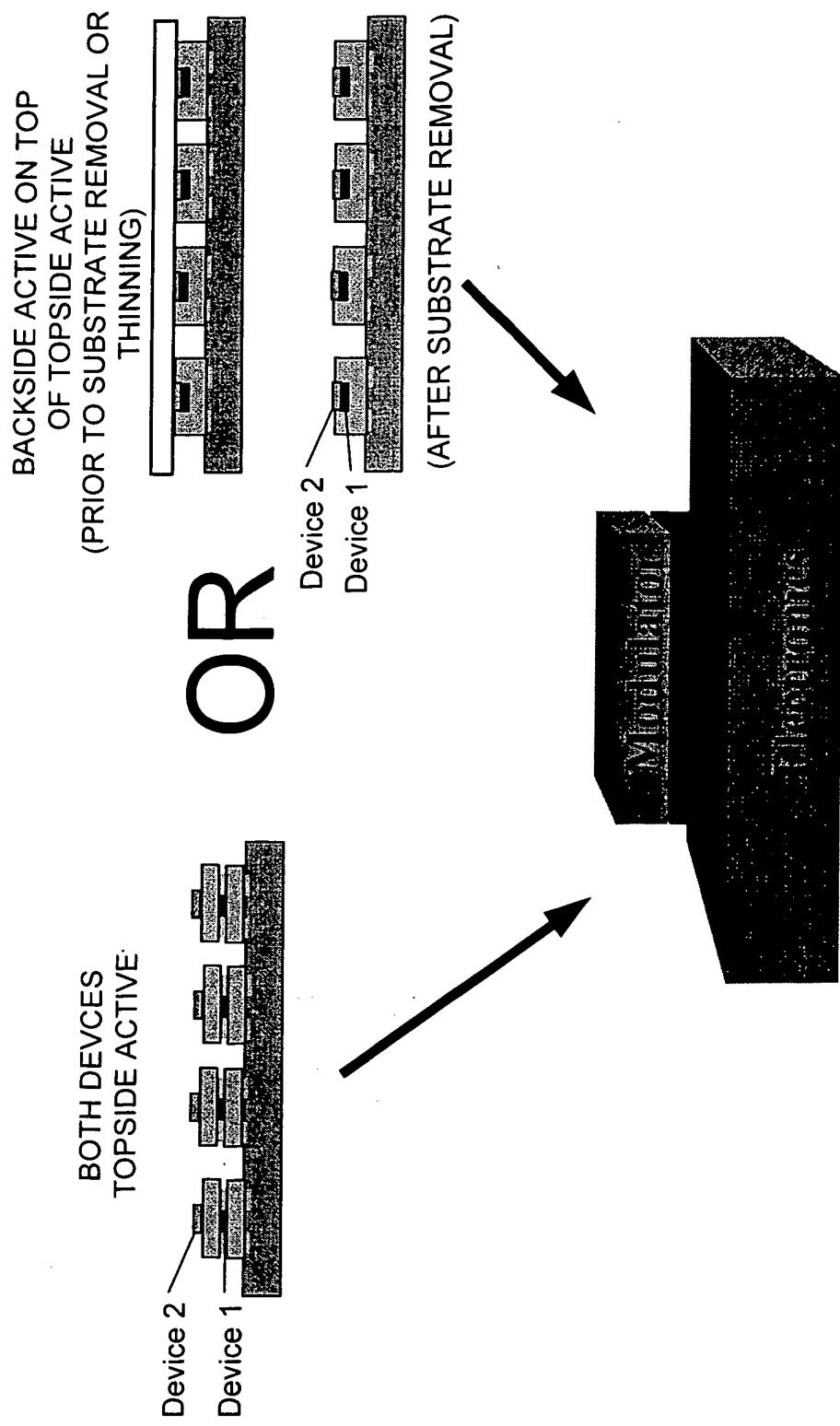


FIG. 20A

பொன்னி கூட்டுரை மேற்கொண்டு வருகிறேன்

$$\begin{aligned}40 \text{ Gb/s} &= 20 \text{ GHz} = 50 \text{ ps} \\ \text{Wavelength in free space} &= 3 \times 10^{10} \text{ cm/s} * 50^{12} \text{ s} = 1.5 \text{ cm} \\ 1/8 \text{ wavelength in } n=3 &= 640 \text{ microns}\end{aligned}$$

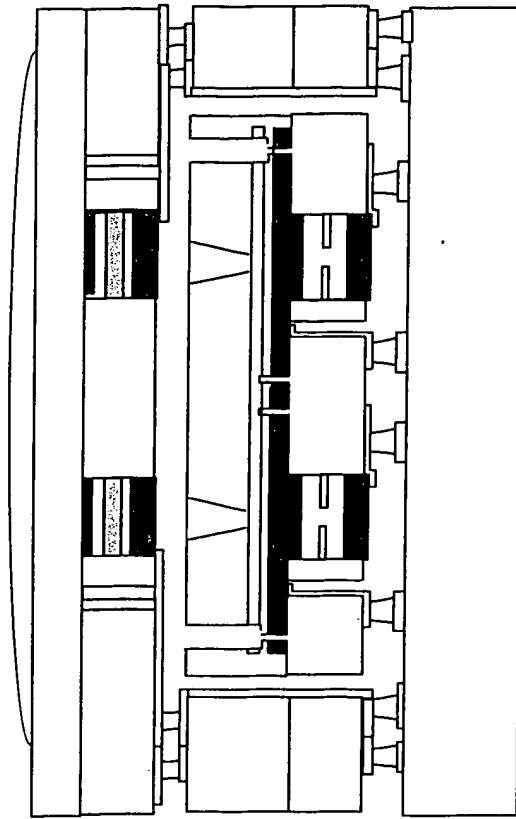
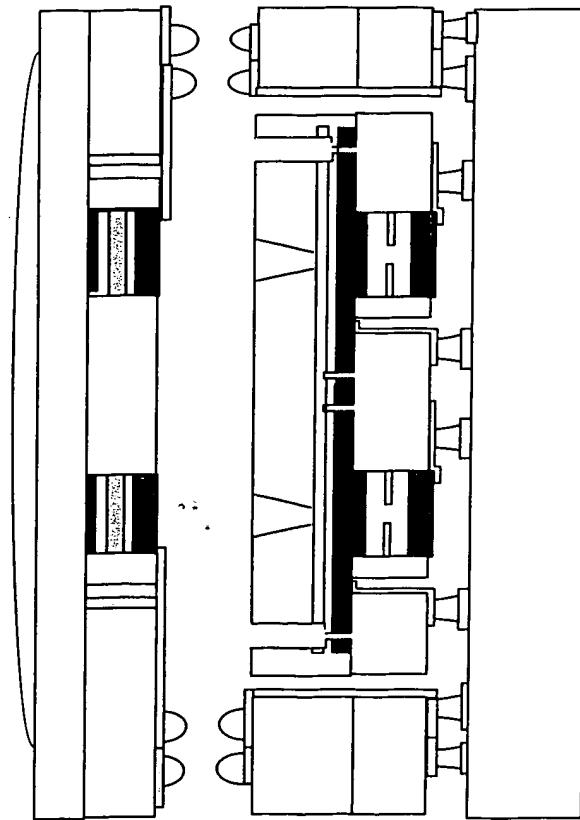


FIG. 20B

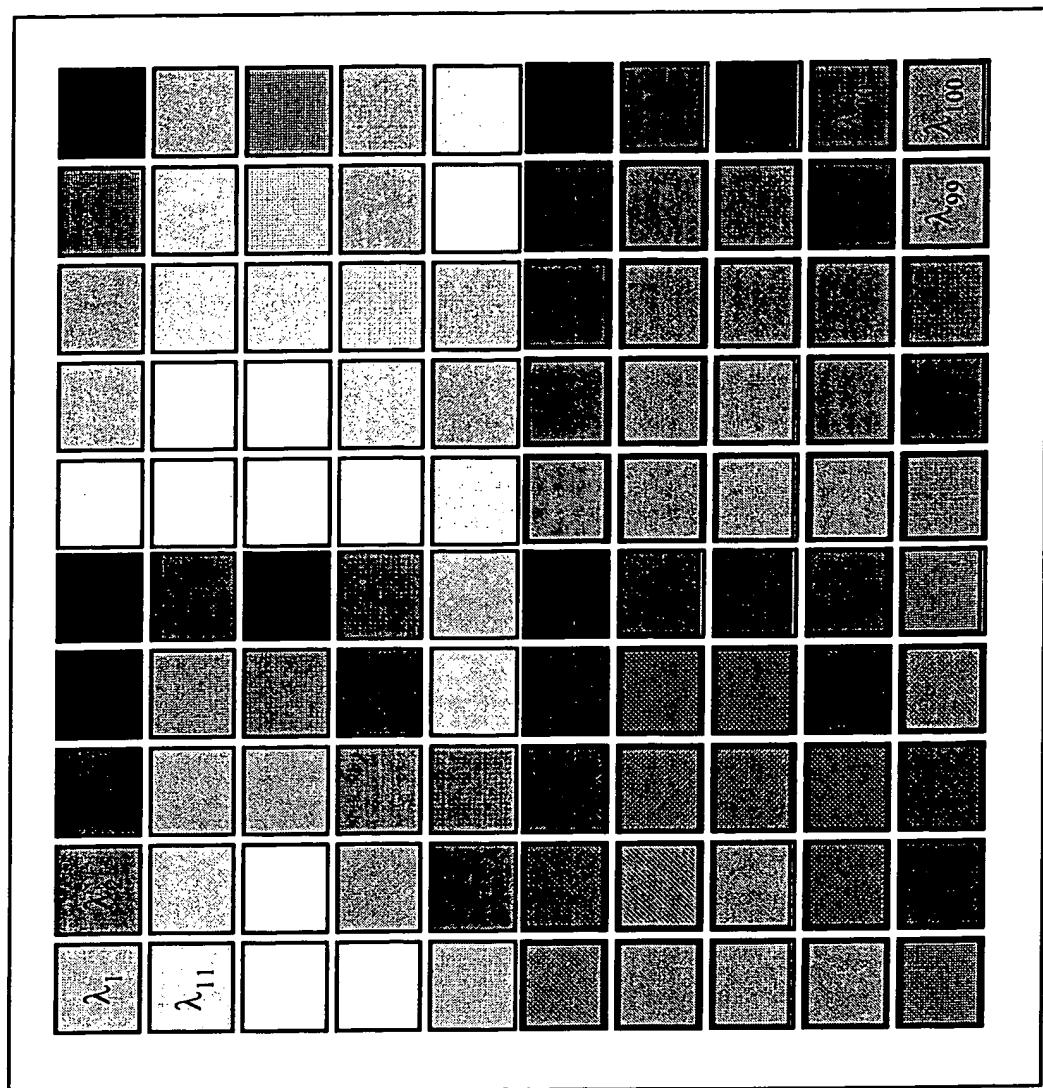


FIG. 21

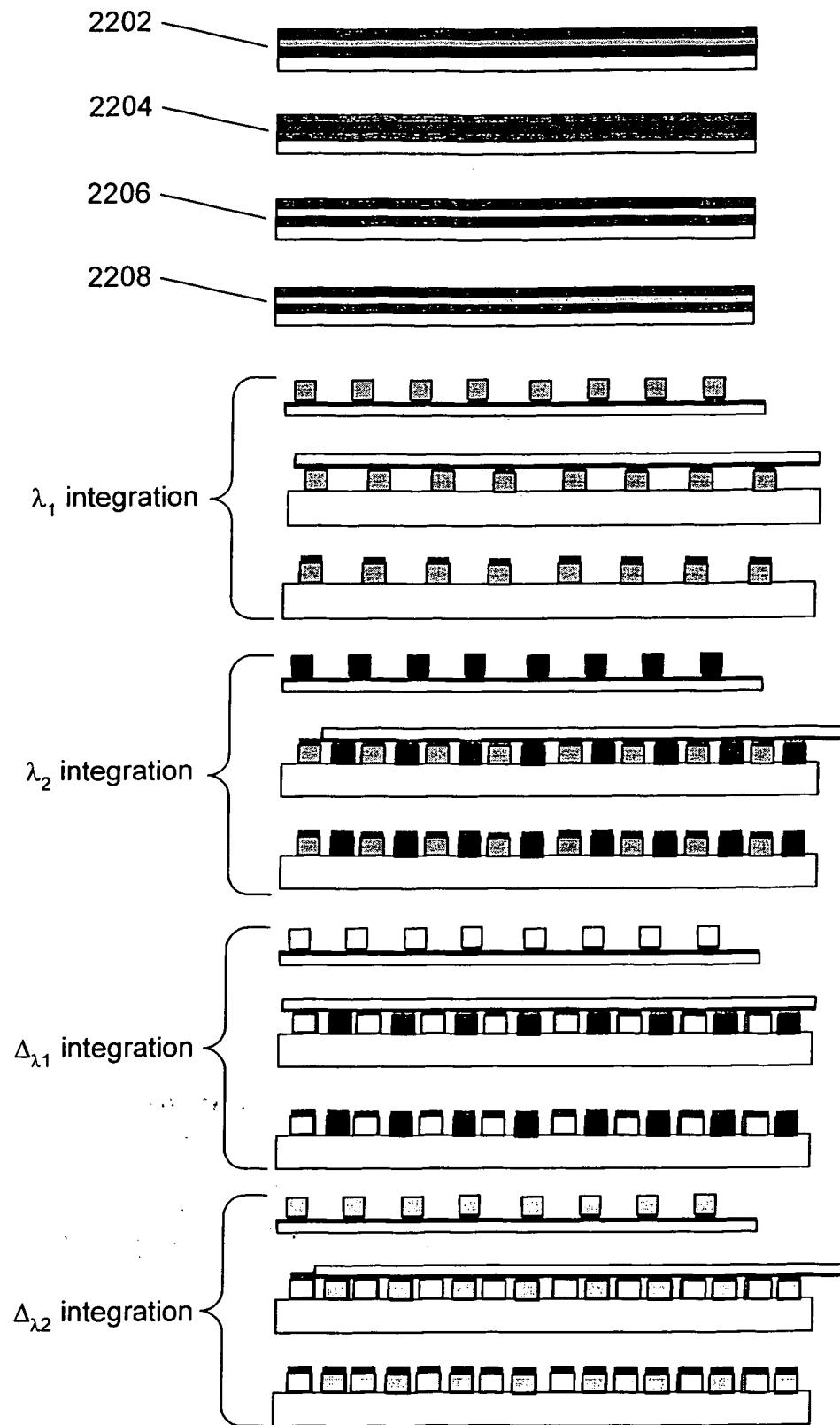


FIG. 22

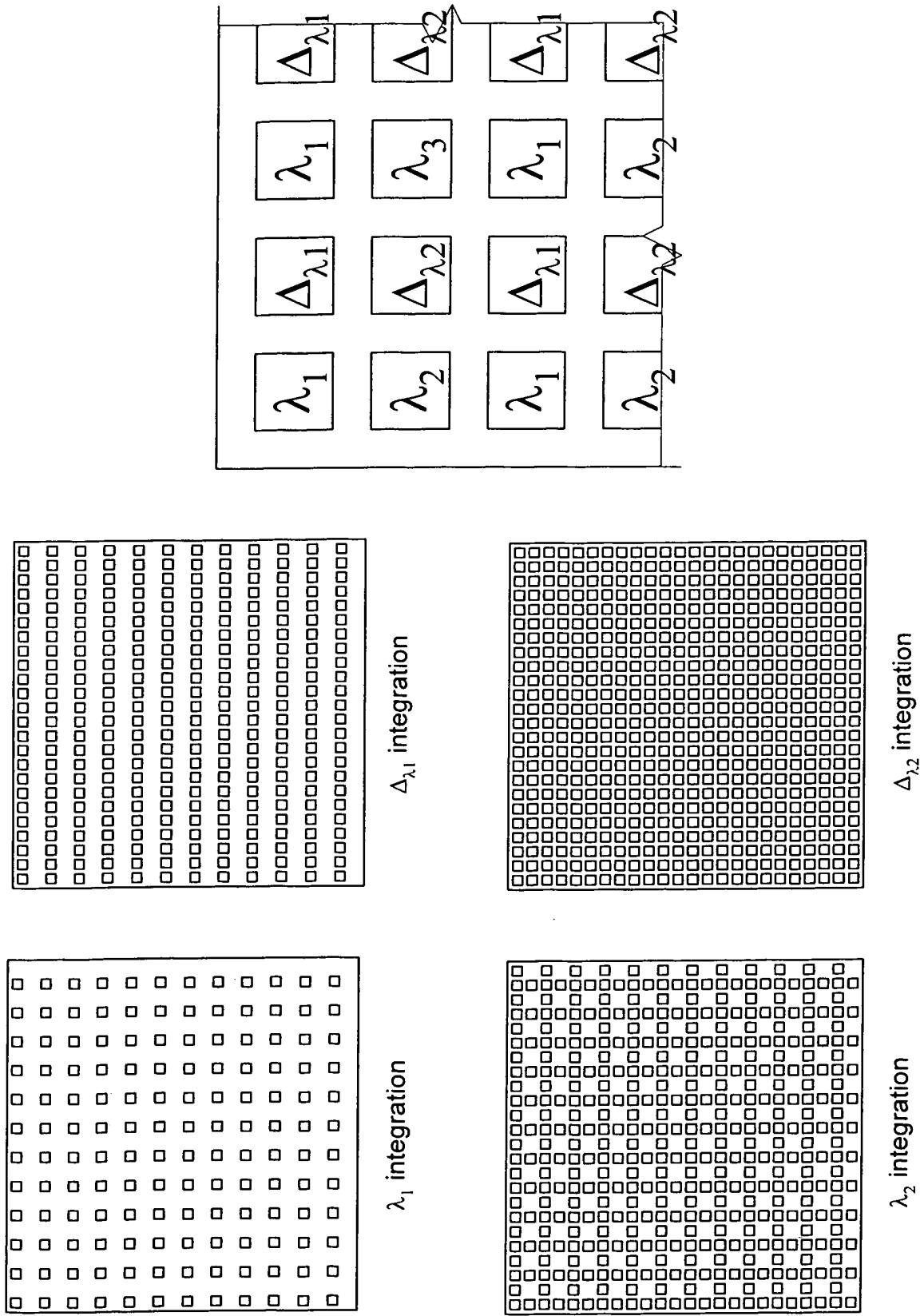


FIG. 23